

First Named

**Inventor:** ERIC ANDRE  
**Patent No.** 6,668,024  
**Issued:** 12/23/2003  
**Application No.:** 09/518,944  
**Filed:** 3/6/2000  
**Title:** RADIOFREQUENCY  
TRANSMITTER WITH A  
HIGH DEGREE OF  
INTEGRATION AND  
POSSIBLY  
WITH SELF-CALIBRATING  
IMAGE DELETION

Examiner:  
Group/Art Unit:  
Atty. Dkt. No: 5957-08400

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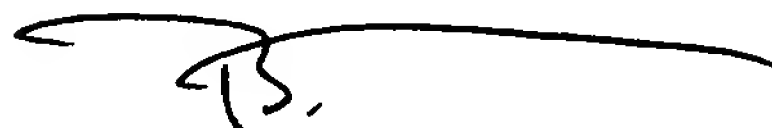
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Respectfully submitted,



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## Patent Assignment Abstract of Title

**NOTE: Results display only for issued patents and published applications.  
For pending or abandoned applications please consult USPTO staff.**

## Total Assignments: 2

**Patent #:** [6668024](#)    **Issue Dt:** 12/23/2003    **Application #:** 09518944    **Filing Dt:** 03/06/2000**Inventor:** Eric Andre**Title:** RADIOFREQUENCY TRANSMITTER WITH A HIGH DEGREE OF INTEGRATION AND POSSIBLY  
WITH SELF-CALIBRATING IMAGE DELETION

## Assignment: 1

**Reel/Frame:** [010885/0707](#)**Recorded:** 06/05/2000**Pages:** 2**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignor:** [ANDRE, ERIC](#)**Exec Dt:** 04/12/2000**Assignee:** [FRANCE TELECOM](#)6, PLACE D'ALLERAY  
75015 PARIS, FRANCE**Correspondent:** MERCHANT & GOULD P.C.JOHN J. GRESENS  
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## Assignment: 2

**Reel/Frame:** [018022/0941](#)**Recorded:** 03/15/2006**Pages:** 10**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignor:** [FRANCE TELECOM INC.](#)**Exec Dt:** 12/03/2004**Assignee:** [FAHRENHEIT THERMOSCOPE, LLC](#)2215-B RENAISSANCE DR. STE 5  
LAS VEGAS, NEVADA 89119**Correspondent:** B. NOEL KIVLINMEYERTONS, HOOD, KIVLIN, KOWERT ET AL  
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5957-05500  
PATENT

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Title: AUTORIZED PERSON  
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 1 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
699,600	US	14/05/1991	Method for manufacturing an active matrix display screen with storage capacitors // Morin, Le Contellec
264,199	US	22/06/1991	Active matrix display screen with storage capacitors formed of conductive blocks, semiconductive material, nonconductive material, and capacitive lines // Morin, Le Contellec
792,262	US	15/11/1991	Method for etching integrated circuit layers to a fixed depth and corresponding integrated circuit // Haond
91403055.6	DE	14/11/1991	
90 14306	FR	16/11/1990	
91403055.6	FR/EP	14/11/1991	
91403055.6	GB	14/11/1991	
764,211	US	23/09/1991	Protective circuit for a control circuit, in particular of liquid crystal display screen // Senn, Lalaj, Martel
764,196	US	23/09/1991	Sample and hold circuit for a liquid crystal display screen // Senn, Lalaj, Martel
836,248	US	18/02/1992	Collector of a bipolar transistor compatible with MOS technology // Nouailhat, Bois
92420047.0	EP	12/02/1992	
9101984	FR	13/02/1991	
832,078	US	06/02/1992	Process for buried localized oxidation of a silicon substrate and corresponding integrated circuit // Straboni, Barla, Wullermoz
9101381	FR	07/02/1991	
866,342	US	10/04/1992	Process for the production of thin film transistors // Chouan
9101618	US	08/07/1992	Method of manufacturing a vertical field effect transistor // Chantre, Bois, Nouailhat
92401909.4	DE	08/07/1992	
92401909.4	GB	03/07/1992	
9108677	FR	10/07/1991	
9101013	US	09/07/1992	Active matrix high definition liquid crystal display structure // Morin, Chouan, Vinanze
92401987.0	GB	09/07/1992	
204,286	JP	09/07/1992	
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2311,024	US	20/04/1994	Method of clock signal recovery and/or synchronization for the reception of information elements transmitted by an ATM network and device for the implementation of the method // Rainard
94400858.1	DE	19/04/1994	
93104775	FR	22/04/1993	
94400858.1	GB	19/04/1994	
228,479	US	15/04/1994	Signal processing device using several different filterings, especially for audio frequency coding of voice signals // Balestro, Senni
94400780.6	DE	11/04/1994	
93104582	FR	16/04/1993	
94400780.6	GB	11/04/1994	
279,546	US	25/07/1994	Optical amplifier having a doped fluoride glass optical fibre and process for producing this amplifier // Ronarc'El, Guilbert, Roumed
94401706.0	GB	25/07/1994	
93109165	FR	26/07/1993	
94401706.0	DE	25/07/1994	
94401706.0	IT	25/07/1994	
299,668	US	02/09/1994	Process for the transposition of an optical modulation of one wavelength to another adjustable wavelength // Dupont, Autreau, Tromeur
94401984.3	GB	07/09/1994	
94401984.3	DE	07/09/1994	
93110737	FR	09/09/1993	
385,958	US	09/02/1995	Degreasing device particularly for optical fibers // Crespel, Gailleaux, Caudrelier
94401673	FR	11/02/1994	
95400232.5	DE	03/02/1995	
95400232.5	GB	03/02/1995	
95400232.5	IT	03/02/1995	
377,472	US	24/01/1995	Optical amplifier with a doped fluoride glass or optical fibre and process for producing said amplifier // Semenkoff, Ronarc'El, Guilbert
492,591	US	20/06/1995	Electronic memory addressing device especially for a memory organized into banks // Quilleyere, Dufill
95401444.5	DE	20/06/1995	
94107579	FR	21/06/1994	
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564,120	US	30/03/1995	Electrically conductive polymer compositions, production process and coated substrates / Clarisse, Deslabouglise, Ciprelli
95915230.7	GB	30/03/1995	
95915230.7	DE	30/03/1995	
94403812	FR	31/03/1994	
527,137	US	12/09/1995	Sequential access asynchronous memory device and corresponding process for storage and reading / Majors, Well
95402213.3	DE	04/10/1995	
94121170	FR	12/10/1994	
95402213.3	GB	04/10/1995	
569,819	US	08/12/1995	Electronic component capable, in particular, of performing a division of two numbers to the base 4 / Dufal, Robert
94115504	FR	22/12/1994	
95402809.8	DE	13/12/1995	
95402809.8	GB	13/12/1995	
639,088	US	24/04/1996	Device for programmable delay of an analog signal and corresponding acoustic antenna / Le Tournear, Balestro
96400851.0	FR/EP	22/04/1996	
96400851.0	DE	22/04/1996	
96400851.0	GB	22/04/1996	
95104911.8	FR	25/04/1995	
08/603,620	US	21/02/1996	Minimizing program code storage for performing regular and repetitive operations in a programmable processor / Dufal, Privat
96400833.9	DE	19/02/1996	
95102131	FR	23/02/1995	
96400833.9	GB	19/02/1996	
674,675	US	02/07/1996	Apparatus and a method for identifying and splicing multicore fibers / Le Noane, Perrin, Le Marec
9614093.4	GB	04/07/1996	
95108042	FR	04/07/1995	
09/462,716	US	08/07/1998	Method of minimising the corner effect by densifying the insulative layer / Schiavonne, Gaillard
2000-502,539	JP	08/07/1998	
98936480.7	GB	08/07/1998	
98936480.7	IT	08/07/1998	
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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 4 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
09/102849	US	23/06/1998	Process for obtaining a transistor having a silicon-germanium gate // Sagnes
98401432.4	EP	12/06/1998	
97-07938	FR	25/06/1997	
178,749	JP	25/06/1998	
094,682	US	15/06/1998	High-density and high-capacity distribution frame for optical fibers // Lamepce, Blanchard, Guéguen
98401410.0	EP	11/06/1998	
97-07892	FR	20/06/1997	
09/384,362	US	26/08/1999	Instrument for measuring the linear and/or circular unit length of multicore fibers // Boscher, Bizeul, Teyssie
99402120.2	EP	25/08/1999	
98-10768	FR	27/08/1998	
09/289,667	US	12/04/1999	Electrooptic method of signal processing, device for implementing the latter, and use // Renard
99400852.2	EP	08/04/1999	
104,365/99	JP	12/04/1999	
98104557	FR	10/04/1998	
09/936,487	US	07/03/2000	Process for testing integrated circuits with access to the memory points of the circuit // Barthel
99102823	FR	08/03/1999	
89104000	IT	07/03/2000	
00909440.0	GB	07/03/2000	
00909440.0	IT	07/03/2000	
09/979,283	US	18/05/2000	Complex number multiplier // Montalvo, Arndt
99106425	FR	20/05/1999	
00929626.0	GB	18/05/2000	
00929626.0	IT	18/05/2000	
09/517,880	US	23/02/2000	Dual mode radio frequency reception device and corresponding multimedia receiver // Andre, Senn
99103769	FR	23/02/1999	
0046001619	EP	24/02/2000	
09/540,188	US	31/03/2000	Process for fabricating a planar heterostructure // Hernandez, Campidelli, Rivoire, Bensahel
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Page 5 of 7			
Filing Number	Country Code	Filing Date	Title / Inventors (US Patent)
09/553,916	US	20/04/2000	Integrated circuit device comprising an inductor with high quality coefficient // Merokell, Rons, Senn, Fournier
99/04986	FR	20/04/1999	
004010864	EP	19/04/2000	
118,658	JP	19/04/2000	
10/019,170	US	23/06/2000	Method for compensating non-linearity of a sigma-delta analog-to-digital converter // Morche
99/08323	FR	29/06/1999	
09/518,944	US	06/03/2000	Radio frequency transmitter with a high degree of integration and possibly with self-calibrating image deletion // Andre
004600193	EP	02/03/2000	
99/03768	FR	23/03/1999	
09/559,524	US	28/04/2000	Frequency control system that stabilizes an output through both a counter and voltage-controlled oscillator via sampling a generated clock into four states // Major
004011003	EP	19/04/2000	
99/05627	FR	30/04/1999	
129,908	JP	28/04/2000	
09/505,448	US	16/02/2000	Process for the anisotropic etching of an organic dielectric polymer material by a plasma gas and application in microelectronics // Joubert, Euard
99/01925	FR	17/02/1999	
030,132	JP	08/02/2000	
004003638	EP	09/02/2000	
10/018,179	US	05/06/2000	Semiconductor device with compensated threshold voltage and method for making the same // Skolnicki, Gwozdzki
009388869	EP	05/06/2000	
99/07391	FR	11/06/1999	
09/980,027	US	26/05/2000	Band-pass filter with carrier frequency reduction // Morche
009369521	GB	26/05/2000	
009369521	JP	26/05/2000	
99/06710	FR	27/05/1999	
10/018,680	US	08/06/2000	Method for making a silicon substrate comprising a buried thin silicon oxide film // Jurczak, Skolnicki
99/07496	FR	14/06/1999	
009404575	EP	08/06/2000	

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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 6 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
09/620,896	US	21/07/2000	Method of correcting topographical effects on a micro-electronic substrate / Schiltz, Paul, Schiavone, Paola
219,525	JP	19/07/2000	
2000-0042258	KR	22/07/2000	
99/09521	FR	22/07/1999	
00402053.3	GB	19/07/2000	
00402053.3	IT	19/07/2000	

01/0168,041	US	08/06/2001	Low-noise spectroscopic ellipsometer // Fernieu
00/07425	FR	09/06/2000	
01945381-0	EP	08/06/2001	
2002-502,401	JP	08/06/2001	

09/912,057	US	25/07/2001	Resin, a double resin layer for extreme ultraviolet (EUV) photolithography, and an extreme ultraviolet light (EUV) photolithography process // Schiltz
01401972.3	EP	23/07/2001	
00/09759	FR	26/07/2000	

10/380,756	US	18/09/2001	Device for punctual measurement of a radiofrequency magnetic field with constant amplitude and frequency // Bouvier, Geoffroy
00/111923	FR	19/09/2000	
01969920.6	DE	18/09/2001	
01969920.6	FR/EP	18/09/2001	
528842/2002	JP	18/09/2001	
01969920.6	GB	18/09/2001	

10/332,532	US	10/07/2001	Saturable absorbent structure, in particular for regenerating optical component and method for making same // Marceau, Loualiche, Le Corre, Delaese
00/09080	FR	11/07/2000	
01954066.7	EP	10/07/2001	
2002-509824	JP	10/07/2001	
2,416,127	CA	10/07/2001	

01/03829	FR	21/03/2001	Method and device for transmitting a video sequence, comprising a face, especially in a mobile videophone system // Roux, Petit
02716894.7	EP	14/03/2002	

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<u>Filing Number</u>	<u>Country Code</u>	<u>Filing Date</u>	<u>Page 7 of 7</u> <u>Title / Inventors</u> <u>(US Patent)</u>
1077251978	US	24/05/2002	Large capacity automatic distributor, particularly for multi-fibre, device and method for automatic connection/ disconnection of the binding fibre; Morellec, Le Traon- Bouchet
02106477	FR	29/05/2001	
0107032	FR	29/05/2001	
027474873	EP	24/05/2002	
2003-800628	JP	24/05/2002	

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on December 3<sup>rd</sup>, 2004

ASSIGNOR

By: France Telecom

Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature **MUST** be notarized)

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la signature de M. François JAMET  
apposée ci-contre  
Paris, le 3 décembre 2004